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CENTRAL FAX CENTERApplication Serial No. 10/575,482
Reply to Office Action of September 22, 2008

MAR 23 2009

PATENT
Docket: CU-4757**Amendments To The Specification**

Please replace the paragraph in the specification at page 3, lines 19-25 with the following amended paragraph:

Figure 1 is a view illustrating a construction of an encryption processor according to a first embodiment of the present invention. As shown in Figure 1, an encryption processor according to a first embodiment of the present invention includes an encryption interface 3, a password ~~process~~ **processor** unit 4, ~~[[and]]~~ memory unit 5, **encryption controller 8, and a communication module 9.** The encryption processor according to a first embodiment of the present invention **also** includes a data input unit 2 for inputting an original data which needs an encryption, a password input unit ~~[[2]]~~ **1** which receives a password ~~[[for]], for example, a personal identification number) number (PIN),~~ needed for encryption, ~~[[and]]~~ an encryption data output unit 7 which receives ~~[[an]]~~ encrypted data from the encryption processor and outputs the same, **and an external input apparatus 7 in communication with the encryption controller 8.**

Please replace the paragraph in the specification at page 3, lines 28-33 with the following amended paragraph:

The encryption interface 3 converts the data inputted from the data input unit 2 and the password input unit 1 connected with the encryption processor into the data, which can be recognized by the password process unit 4 or the memory unit 5. The encryption interface 3 converts the data processed by the password process unit 4 or the memory unit 5 into the data which can be recognized by the encryption data output unit ~~[[7]]~~ **6.**

Please replace the paragraph in the specification at page 3, lines 34-36 with the following amended paragraph:

The **encryption password** processor **unit 4** receives a password, which is needed for a data code verification, from the externally connected password input unit 1 and creates a verification data and encrypts the original data based on a certain encryption algorithm.

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Please replace the paragraph in the specification at page 6, lines 21-24 with the following amended paragraph:

The key data inputted through the password input apparatus 102 is preferably directly transferred to the encryption module 510, not through the camera controller **[[5]] 101** which drives the digital camera. It is possible to prevent any leakage of the password (for example, PIN) by directly transmitting the key data to the encryption module 510.

Please replace the paragraph in the specification at page 8, lines 19-23 with the following amended paragraph:

When the camera controller **[[5]] 101** requests a certain video data display to the display unit 105, the video data stored in the memory 104 are transferred to the password process unit 512 through the second interface 521. The video data are processed with the decoding algorithm corresponding to the encoding algorithm of the password process unit 512 for thereby decoding the encrypted video data.

Please replace the paragraph in the specification at page 9, lines 19-25 with the following amended paragraph:

The video process module **[[520]] 620** includes a second interface which manages the connections of the input and output apparatus including a second interface 621 which includes a camera controller 101, a memory 104, a USB 103, a display unit 105 and a video data generation unit 13. The video process module **[[520]] 620** further includes a video adjusting unit 14 which adjusts the recording environment including focus, exposure and lighting of the video data, and a decoder 623 which decrypts the compression of the compressed video data.

Please replace the paragraph in the specification at page 10, lines 3-10 with the following amended paragraph:

The encryption processor 70 is formed of a single chip and converts an electrical signal from the signal compensation unit 12 into a video data and checks a recording environment including focus, exposure and lighting of the video data and controls the operation of the optical system 10, the CCD 11 or the signal compensation unit 12. The encryption processor **[[60]] 70** converts the video data including luminance and color difference signal of an object into a video data having a format such as AVI or MPEG through a data encoding process and converts the video into an encrypted video data through a certain encryption algorithm.

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Please replace the paragraph in the specification at page 10, lines 16-19 with the following amended paragraph:

The encryption module 710 of Figure 7 is the same as the encryption module 510 of Figure 3 in their constructions and functions. Therefore, the detailed descriptions of the encryption module 710 will be referred to the encryption module 510 of Figure **[[2]] 3** and will be omitted.

Please replace the paragraph in the specification at page 10, lines 20-28 with the following amended paragraph:

The video process module 720 includes a second interface **[[621]] 721** which manages the connection of the input and output apparatus including a camera controller 101, a memory 104, a USB 103, a display unit 105 and a signal compensation unit 12. The video process module 720 further includes a video data generation unit 13 which converts the electrical signal from the signal compensation unit 12 into a video data including luminance and color difference signal of an object, a video adjusting unit 14 which adjusts the recording environment including focus, exposure and lighting of the video data, a coder 622 which compresses the video data into a certain format, and a decoder 623 which decompresses the compressed video data.

Please replace the paragraph in the specification at page 11, lines 12-15 with the following amended paragraph:

Figure **[[1]] 10** is a view illustrating a construction of an encryption processor 80 provided at a digital camera according to a fifth environment of the present invention. As shown in Figure 9, the encryption processor 80 is formed of a single **[[ship]] chip** and includes an encryption module 810 and a video process module 820.

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Please replace the paragraph in the specification at page 11, lines 20-29 with the following amended paragraph:

The video process module 820 includes a second interface 821 which manages the connection of the input and output apparatus including a camera controller 101, a memory 104, a USB 103, a display unit **[[9]] 105** and a CCD 11. The video process module 720 further includes a signal ~~compensation~~ **adjusting** unit 12 which removes the noises from the electric signal from each pixel, a video data generation unit 13 which converts the electrical signal from the signal ~~compensation~~ **adjusting** unit 12 into a video data including luminance and color difference signal of an object, a video adjusting unit 14 which adjusts the recording environment including focus, exposure and lighting of the video data, a coder 622 which compresses the video data into a certain format, and a decoder 623 which decompresses the compressed video data.

Please replace the paragraph in the specification at page 13, lines 4-5 with the following amended paragraph:

First, it is possible to basically prevent the leakage of digital data. Only **[[only]]** a verified apparatus can access the data.